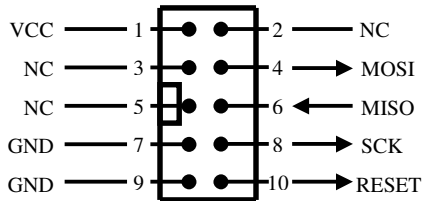


### 10-Pin IDC Header Top View



### Micro to ISP – Target System Connection Requirements

The programmer 10-pin ribbon cable supplied is terminated with a standard 10-pin 0.1" pitch IDC polarized plug. This is designed to mate with a male 10-pin IDC header on the target system. The pin-out of the header is shown at the left.

*Note: Connector shown is the male 10-pin IDC connector as viewed from above the component side of the target board.*

### Pin Assignments

Pin	Name	Description	I/O
1	Vcc	Programmer Power (+Vcc, 5V DC)	-
2	NC	No Connect	-
3	NC	No Connect	-
4	MOSI	SPI – Master Out Slave In	O
5	NC	No Connect	-
6	MISO	SPI – Master In Slave Out	I
7	GND	Programmer GND	-
8	SCK	SPI – Serial Clock	O
9	GND	Programmer GND	-
10	RESET	Target RESET Control Pin	O

### Reset Circuit

TheCableAVR hardware must be able to externally control the state of the RESET pin.

### Power

TheCableAVR hardware is powered from the target application. Pins 1 and 9 are VCC and GND, respectively. When powered, TheCableAVR's green LED is lit.

### Target System Oscillator

The target system oscillator must be running for TheCableAVR to access the target processor. The frequency must be an acceptable frequency for the target processor. The acceptable frequencies can be found in the microcontroller data sheets.

Note: The oscillator could be an external crystal/resonator or an internal RC oscillator.

### Priio

8645 Guion Road  
Suite A  
Indianapolis, IN 46268  
317 | 471.1577  
317 | 471.1580 (fax)  
www.priio.com

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